# InspireSemi™

Thunderbird<sup>™</sup>
Disruptive Next Generation HPC-Al
Accelerated Computing Platform

Business Update September 18, 2025

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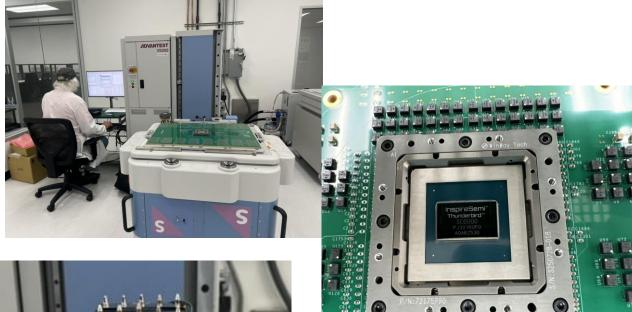
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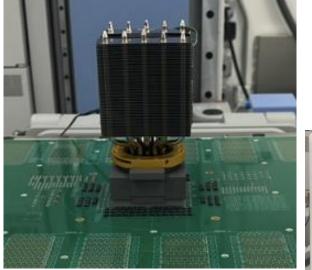
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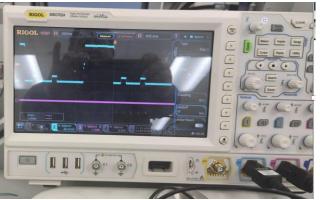


#### Thunderbird is Alive!

- Thunderbird A0 silicon was delivered Saturday September 13, 2025
- A comprehensive validation process is underway, but the preliminary test results look very promising
- This is the most important milestone in the history of the company thus far
- The Thunderbird accelerated computing platform remains unprecedented in the industry and addresses a major unmet need

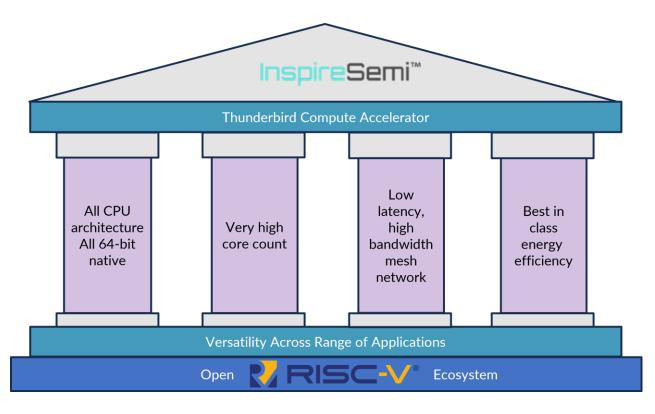








#### **Thunderbird Value Proposition**



- Versatile all-CPU architecture applicable to all HPC-Al software, much of which does not benefit from GPUs
- Thousands of high precision, high performance, low power 64-bit native processors to solve "big math" problems required by most HPC software



#### Thunderbird datacenter impact:

Significant datacenter TCO savings and carbon footprint reduction

**One Thunderbird board** has more CPU cores than an **entire** rack of standard Intel or AMD severs!

- Less cost: real estate, servers, networking, power, cooling
- Less complexity, interconnects, points of failure



Thunderbird has >20X CPU cores vs. Intel or AMD server chips

- Intel and AMD CPUs: Up to 256 general purpose x86 cores per server board
- Thunderbird: 6,144 high-performance/low-power RISC-V cores per add-in board (all 64-bit)



3U Server with 8 Thunderbird cards







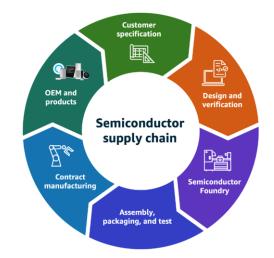
### **High Momentum Roadmap**

- A0 silicon is an important milestone
  - First silicon demonstrates the promise of the full Thunderbird value proposition
  - Enables validation and characterization
- 1Q'26: A1 delivers on the promise and builds company momentum
  - Software ecosystem partners do full porting/tuning
  - OEM partner qualifications
  - Key learnings and feedback for roadmap
  - Continued growth of team and IP portfolio
- B0 will scale production
  - Additional feedback to inform Thunderbird II feature set



### Poised for explosive scale

- Highly differentiated solution
  - Strong competitive positioning
  - Versatile solution; Well placed cover for multiplicity of attractive markets
- High margin, low OPEX efficient business model
  - 80+% Gross Margins
  - 35+ Thunderbird I cards sales per month ensures B/E
- Supply chain primed, no glitches, skids greased
  - Initiating initial substrate orders
  - Capacity reserved and allocated
  - Preparing automated test capability
  - Early engineering lots will support first customer sales
- Design and production partners remain 'all-in'
  - Cadence fully committed to both 12nm and 4nm/3nm tools
  - TSMC granted access to coveted to 3nm libraries
  - ASE allocated capacity in the ChungLi factory
  - Imec supporting substrate design and Eng/pilot builds
- Attracting top talent on JIT basis
  - Critical new skills and bench strength















## **Annual General Meeting**

- AGM likely to be held in December, 2025
- AGM will be in person only in Austin, TX in order to preserve costs
- Materials will be mailed to shareholders (only those registered) approximately 3 weeks before the Meeting
- Company will press release date when confirmed
- Any shareholder who doesn't receive materials can email Odyssey direct to receive by email at shareholders@odysseytrust.com



## **CEO Transition**

- The Company announced that Ron Van Dell will be stepping down from his role as CEO of the Company at the end of his current contract effective December 31, 2025
- While Ron will remain with the Company as a member of the Board of Directors, Ron has decided now is the time for him to retire from full time executive work
- The Company's Executive Chairman, James Hickman who has been with the Company for over 7 years, is expected to assume the additional role of interim CEO
- Ron and James will be working closely together over the remaining time of Ron's contract to ensure a smooth transition into 2026

# Chairman's Thoughts



# Q&A

